



## Product Description

With its Higgs-3 / Higgs-4 Chip, the Taurus Inlay delivers next generation read and write performance.

It has flexible memory architecture that provides for the optimum allocation of EPC and User memory for different use cases such as legacy part numbering systems and service history. User memory can also be read and or write locked on 64-bit boundaries, supporting a variety of public/private usage models.

The IC also features a factory programmed 64-bit serial number that cannot be altered. In conjunction with the EPC code, this provides a unique “fingerprint” for the tagged item.



## Specifications

Dimensions	101.60 x 25.40 mm ( 4 x 1" )	
Pitch	28.575 mm ( 1.125" )	
RF Protocol	ISO 18000-6C EPC C1G2	
Data Transfer Rate	Up to 640 kbps	
Operating Frequency	860-960 MHz	
ESD – HBM / CDM	>5.0 kV / >1.5 kV	
Operating Temperature (Installed)	-50°C to +85°C (-58°F to +185°F)	
Storage Temperature	-50°C to +100°C (-58°F to +212°F)	
Relative Humidity (RH) (Installed)	100% Condensing Humidity	
Chip Type	Alien H3 chip	Alien H4 chip
EPC	96-480 Bits	128 Bits
On-Chip Memory	800 Bits	448 Bits
Unique TID	64 Bits	64 Bits
Access Password	32 Bits	32 Bits
Kill Password	32 Bits	32 Bits
EEPROM Data Retention	50 Years	50 Years
EEPROM Write Endurance	100,000 Cycles	100,000 Cycles
Custom TID Bitmask	Optional	Optional
Password Authentication	Yes	Yes